



PATENT
Att'y Dkt.: 2207/10607

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SOLOMON et al

Serial No.: 09/892,566

Filed: June 28, 2001

For: POWER REDUCTION FOR PROCESS
FRONT-END BY CACHING DECODED
INSTRUCTIONS

Examiner: G.J. Portka

Confirmation No. 2824

Art Unit: 2188

Assignee: Intel Corporation

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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JUL 15 2004

Technology Center 2100

Sir:

Applicants respond to the outstanding restriction requirement as follows:

REMARKS

The application contains claims 1-15 and 23-26. In response to the restriction requirement, Applicants elect Group I, containing claims 1-6 and 10-15. The election is made with traverse.

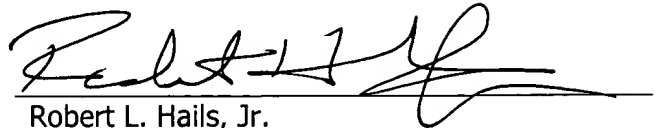
Restriction is proper only if examination of all pending claims poses an undue burden upon the Examiner. See, MPEP § 803.01 (Examiner **must** examine entire application if it can be made without serious burden). No such burden exists in this case. Independent claim 7, for example, recites elements that are similar to those of dependent claim 4. Assuming the Office will conduct an examination of **all** claims in the elected group, there is little if any increased burden to examine the claims of the non-elected group. For example, Applicants respectfully suggest that the Examiner should search class 711, subclasses 118 and 119 simply to examine the claims of the elected group. After all, the PTO-892 that accompanies the Feb. 2004 Office Action, paper no. 7, indicates that search materials for this application have been drawn from multiple subclasses of class 711 already – subclasses 118 and 125.

Serial No. 09/892,566
Inventor: SOLOMON et al
In Response to Office Action of June 30, 2004

The Examiner faces no undue burden to examine all claims pending in the application.
Accordingly, the restriction requirement should be withdrawn.

Respectfully submitted,

Date: July 13, 2004



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